

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Applicant: St. Lawrence )  
Serial No.: 10/613,924 ) Group Art Unit: 2826  
Filing Date: July 3, 203 ) Examiner: Erdem  
For: CIRCUIT MATERIALS, CIRCUITS, )  
MULTI-LAYER CIRCUITS, AND )  
MEHTODS OF MANUFACTURE )  
THEREOF )

Commissioner for Patents  
P. O. Box 1450  
Alexandria, VA 22313-1450

**INFORMATION DISCLOSURE STATEMENT**  
**UNDER 37 CFR §§ 1.56, 1.97 AND 1.98**


Sir:

In compliance with the duty to disclose, submitted herewith is form PTO-1449 listing publication(s) of which those designated by 37 CFR § 1.56 are aware. Copies of the non-United States patents or published applications are enclosed.

The filing of this Information Disclosure Statement shall not be construed as a representation that a search has been made, or an admission that the information cited is, or is considered to be, material to patentability.

Respectfully submitted,

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